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Application Number	10/673,692
Filing Date	September 29, 2003
First Named Inventor	Kyle K. Kirby
Group Art Unit	2823
Examiner Name	M. Estrada
Attorney Docket Number	2269-5665US (02-1291.00/US)

ENCLOSURES (check all that apply)

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<input checked="" type="checkbox"/> Amendment in response to office action dated March 28, 2005	<input type="checkbox"/> Petition	
<input type="checkbox"/> Amendment under 37 C.F.R. § 1.116 in response to final office action dated	<input type="checkbox"/> Fee Transmittal Form	
<input type="checkbox"/> Additional claims fee - Check No. in the amount of \$	<input type="checkbox"/> Certified Copy of Priority Document(s)	<input type="checkbox"/> Other Enclosure(s) <i>(please identify below):</i>
<input type="checkbox"/> Letter to Chief Draftsman and copy of FIGS. with changes made in red	<input type="checkbox"/> Assignment Papers <i>(for an Application)</i>	
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Remarks		
The Commissioner is authorized to charge any additional fees required but not submitted with any document or request requiring fee payment under 37 C.F.R. §§ 1.16 and 1.17 to Deposit Account 20-1469 during pendency of this application.		

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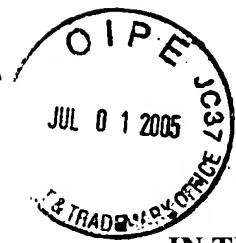
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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Kyle K. Kirby

Serial No.: 10/673,692

Filed: September 29, 2003

For: METHOD FOR CREATING
ELECTRICAL PATHWAYS FOR
SEMICONDUCTOR DEVICE
STRUCTURES USING LASER
MACHINING PROCESSES

Confirmation No.: 4168

Examiner: M. Estrada

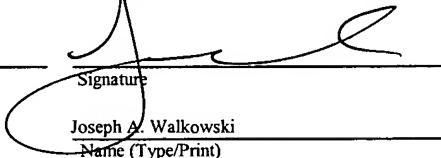
Group Art Unit: 2823

Attorney Docket No.: 2269-5665US
(02-1291.00/US)

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Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
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Sir:

The following amendments and remarks are filed in response to the Examiner's remarks in the Office Action mailed March 28, 2005, the three-month shortened statutory period for response to which expires on June 28, 2005.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 8 of this paper.